P.12

Application Serial No. 09/676,162 Amendment dated August 13, 2004 Reply to office action dated May 24, 2004

Remarks/Arguments

This Amendment is being filed in response to the Office Action of the Examiner mailed May 24, 2004, setting a three-month shortened statutory period for response ending August 24, 2004. Claims 1-40 remain pending. Claims 41-52 have been canceled without prejudice as being directed toward a non-elected invention. Reconsideration, examination and allowance of all pending claims are respectfully requested.

In paragraph 1 of the Office Action, the Examiner restricted the pending claims into two groups, namely: Group I corresponding to claims 1-40; and Group II corresponding to claims 41-52. In paragraph 4, the Examiner noted that on May 7, 2004, the undersigned elected claims 1-40, without traverse. Applicants hereby confirm this election.

In paragraph 7 of the Office Action, the Examiner rejected claims 3, 13-15, 32, 39 and 40 under 35 U.S.C. § 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. In response, Applicants have amended the claims to overcome the Examiner's rejections.

In paragraph 15 of the Office Action, the Examiner rejected claims 1-9, 16-26 and 33-40 under 35 U.S.C. § 102(b) as being anticipated by Campbell et al. (U.S. Patent No. 5,365,587). Claim 1, as amended, recites:

1. (Currently Amended) A method for selectively increasing the performance of a customer's data processing system, wherein the data processing system has a maximum performance level, the method comprising:

providing a first authorization key to the data processing system, the first authorization key allowing an initial performance level that is less than the maximum performance level of the data processing system;

receiving a request from the customer for an increase in performance level of the data processing system; and

providing a second authorization key that to increase[[s]] the performance level of the data processing system above the initial performance level; and

increasing the performance level of the data processing system in accordance with the second authorization key before a next time the data processing system is powered up.

As can be seen, claim 1 recites that the performance level of the data processing system is increased before a next time the data processing system is powered up. This amendment is supported by the present specification at, for example: page 5, lines 12-21; page 8, lines 18-20; and page 10, lines 21-23.

The present specification teaches that the performance level of the customer's data processing system can be changed while operating at full production capacity, and that such a system may help provide uninterrupted growth capacity, temporary increased processing capacity to support high demand peak periods, and disaster recovery support should a primary data processing system become destroyed.

In contrast to the foregoing, Campbell et al. states:

Upon reference to the foregoing those skilled in the art will appreciate that by providing selected "personality" data within nonvolatile storage devices within a data processing system and accessing that data each time power is applied to the data processing system, the content of a personality register may be controlled which may then be utilized to enable and/or disable various functional characteristics of the data processing system, permitting the manufacturer to provide a variety of functional characteristic capabilities within a single data processing system, without physical or mechanical manipulation.

(Emphasis Added)(Campbell et al., column 9, lines 54-65). As can be seen, Campbell et al. appear to teach that the "functional characteristics" of the data processing system are only changed upon power up of the data processing system. However, such a system may not provide some of the advantages of the present invention - such as providing uninterrupted growth capacity, temporary increased processing capacity to support high

demand peak periods, and disaster recovery support should a primary data processing system become destroyed - at least not to the same degree as the present invention.

In addition, Campbell et al. do not appear to disclose or suggest any way to increasing the performance level of the data processing system before a next time the data processing system is powered up, as recited in claim 1. In view thereof, claim 1 is believed to be clearly patentable over Campbell et al. For similar reasons, as well as other reasons, dependent claims 2-5 are also believed to be clearly patentable over Campbell et al.

Turning now to claim 6, which recites:

6. (Currently Amended) A method for selectively changing the performance of a data processing system, wherein the data processing system includes one or more processors that can selectively operate at a performance level that is below a maximum performance level of the processor, the method comprising:

providing an authorization key to the data processing system, wherein the authorization key specifies a new performance level for at least one of the processors; and

increasing the performance level of at least one processor to the new performance level before a next time the data processing system is powered up.

As can be seen, claim 6 recites the step of increasing the performance level of at least one processor to the new performance level before a next time the data processing system is powered up. For similar reasons to those discussed above with respect to claim 1, as well as other reasons, claim 6 is believed to be clearly patentable over Campbell et al. For similar and other reasons, dependent claims 7-23 are also believed to be clearly patentable over Campbell et al.

Now turning to claim 24, which recites:

24. (Currently Amended) A method for selectively changing the performance of a data processing system, wherein the data processing system includes two or more processors and an original limit is placed on the number of processors that are available for use, the method comprising:

providing an authorization key to the data processing system, wherein the authorization key specifies a new limit on the number of processors that are available for use; and

increasing the performance level of the data processing system by activating one or more of the processors that were previously unavailable for use before a next time the data processing system is powered up.

As can be seen, claim 24 recites the step of increasing the performance level of the data processing system by activating one or more of the processors that were previously unavailable for use before a next time the data processing system is powered up. Thus, for similar reasons to those given above with respect to claim 1, as well as other reasons, claim 24 is believed to be clearly patentable over Campbell et al. For similar and other reasons, dependent claims 25-40 are also believed to be clearly patentable over Campbell et al.

Turning specifically to claim 33, and as detailed above, Campbell et al. do not disclose or suggest performing the providing and increasing steps while the data processing system is in use. As noted above, Campbell et al. appear to teach to change the "functional characteristics" of the data processing system upon power up of the data processing system, which by definition, cannot be "while the data processing system is in use".

Specifically with respect to claim 35, Campbell et al. do not appear to teach or suggest using the <u>operating system</u> of the data <u>processing system</u> to activate one or more processors.

With respect to claim 36, Campbell et al. do not appear to teach or suggest a data processing system wherein the operating system maintains a table that includes entries that identify the processors in the data processing system, and further identify which processors are available for use. With respect to claim 37, Campbell et al. do not appear to teach or suggest changing selected entries in the table to indicate that one or more of the processors that were previously unavailable for use are now available for use. With respect to claim

38, Campbell et al. do not appear to teach or suggest an operating system that detects the changes to the table, and ups the processors that are indicated as available for use that were previously unavailable for use. With respect to claim 39, Campbell et al. do not appear to teach or suggest changing selected entries in a table so that selected processors that are available for use are de-activated and become unavailable for use to return to the original limit of the number of processors that are available for use. Finally, and specifically with respect to claim 40, Campbell et al. do not appear to teach or suggest an operating system that detects the changes to the table, and downs the processors that are indicated as unavailable for use.

As such, and for these additional reasons, claims 25-40 are believed to be clearly patentable over Campbell et al. If the Examiner elects to maintain these rejections, Applicants respectfully request that the Examiner specifically point out where in Campbell et al. each and every one of these elements is shown.

In paragraph 16 of the Office Action, the Examiner rejected claims 1-23 under 35 U.S.C. § 102(b) as being anticipated by Fenstemaker et al. (U.S. Patent No. 6,490,684). As noted above, claim 1, as amended, recites:

1. (Currently Amended) A method for selectively increasing the performance of a customer's data processing system, wherein the data processing system has a maximum performance level, the method comprising:

providing a first authorization key to the data processing system, the first authorization key allowing an initial performance level that is less than the maximum performance level of the data processing system;

receiving a request from the customer for an increase in performance level of the data processing system;

providing a second authorization key to increase the performance level of the data processing system above the initial performance level; and increasing the <u>performance level</u> of the data processing system in accordance with the second authorization key before a next time the data processing system is powered up.

(Emphasis Added). As can be seen, claim 1 recites that the <u>performance level</u> of the data processing system is increased. In contrast to the foregoing, Fenstemaker et al. appears to relate to changing the <u>functionality</u> (and not the performance level) of an ultrasound imaging system.

Fenstemaker et al. state:

By way of introduction, in the preferred embodiments described below, an ultrasound device is installed with <u>features</u> that are not immediately available for use (i.e., the features are installed disabled). To enable these <u>features</u>, a key is locally or remotely supplied to the ultrasound device on demand. With this functionality, a user can evaluate a <u>feature</u> on a trial basis. After becoming familiar with the <u>feature</u>, the user can decide to purchase the <u>feature</u> for permanent use. Additionally, because the necessary hardware is factory-installed in and shipped with the ultrasound device, a user who desires the temporary or permanent use of a hardware <u>feature</u> can enable the <u>feature</u> simply by receiving the enabling key on demand without taking the ultrasound device off-line.

(Emphasis Added)(Fenstemaker et al., column 1, lines 37-50). Fenstemaker et al. define a feature as "a function of an ultrasound device" (Emphasis Added)(Fenstemaker et al., column 2, lines 12-13). Therefore, and as can readily be seen, Fenstemaker et al. relates to changing the functionality of an ultrasound imaging system, and not the performance level of a data processing system as recited in claim 1. Merriam-Webster's dictionary defines the term level as "the magnitude of a quantity considered in relation to an arbitrary reference value". Fenstemaker et al. is not changing the magnitude of a quantity (e.g. processing speed), but rather is adding additional functionality.

Fenstemaker et al. state:

A feature can be implemented with hardware, software, or a combination of hardware and software. For example, on an ultrasound imaging system, a hardware feature can be a use of a particular transducer, a software feature can be harmonic imaging, and a combination of hardware

and software features can be the use of harmonic imaging with a particular transducer.

(Fenstemaker et al., column 14-19). Adding the use of an additional transducer or a software function such as harmonic imaging does not increase the <u>performance level</u> of a data processing system, as recited in claim 1. If anything, the addition of these features may decrease the performance level of the data processing system, as more processing resources may be required to support these features. There does not appear to be anything in Fenstemaker et al. that suggests the step of increasing the <u>performance level</u> of a data processing system, as recited in claim 1.

Applicants believe there is a big difference between enabling the use of a particular transducer or an additional software routine in an ultrasound machine versus increasing the performance level of a data processing system, such as by increasing the operating speed and/or number of processors that are available to the operating system. It is a completely different problem to change the performance level of a data processing system, particularly while the data processing is in use (e.g. without taking down the data processing system). As noted above, Fenstemaker et al. does not appear to teach or suggest anything that relates to increasing the performance level of a data processing system, and thus the relevance of Fenstemaker et al. is suspect.

In view of the foregoing, claim 1 is believed to be clearly patentable over Fenstemaker et al. For similar reasons, as well as other reasons, dependent claims 2-5 are also believed to be clearly patentable over Fenstemaker et al.

Turning now to claim 6, which recites:

6. (Currently Amended) A method for selectively changing the performance of a data processing system, wherein the data processing system includes one or more processors that can selectively operate at a

P.19

Application Serial No. 09/676,162 Amendment dated August 13, 2004 Reply to office action dated May 24, 2004

performance level that is below a maximum performance level of the processor, the method comprising:

providing an authorization key to the data processing system, wherein the authorization key specifies a new performance level for at least one of the processors; and

increasing the performance level of at least one processor to the new performance level before a next time the data processing system is powered up.

As can be seen, claim 6 recites the step of increasing the <u>performance level</u> of at least one processor to the new performance level before a next time the data processing system is powered up. For similar reasons to those given above with respect to claim 1, as well as other reasons, claim 6 is believed to be clearly patentable over Fenstemaker et al. For similar and other reasons, dependent claims 7-23 are also believed to be clearly patentable over Fenstemaker et al.

In paragraph 18 of the Office Action, the examiner rejected claims 1-40 under 35 U.S.C. § 103(a) as being unpatentable over Campbell in view of Fenstemaker. After reviewing the rejection, Applicants believe the Examiner may have made a typographical error in this rejection. From the Examiner remarks in paragraph 18, Applicants believe that the Examiner meant to reject only claims 24-26 and 33-40 under U.S.C. § 103(a) as being unpatentable over Campbell in view of Fenstemaker. Applicants respectfully request clarification if this is not correct.

In paragraph 18, the Examiner states that Fenstemaker discloses a method to increase the performance of a processing system, but does not mention controlling the number of active processors. However, the Examiner states that Campbell discloses a method to increase the performance of a processing system by controlling the number of active processors. The Examiner concludes that it would have been obvious to one of

P.20

Application Serial No. 09/676,162 Amendment dated August 13, 2004 Reply to office action dated May 24, 2004

ordinary skill in the art to increase the number of processors in Fenstemaker's system to allow for further profitability and control.

Claim 24 recites:

24. (Currently Amended) A method for selectively changing the performance of a data processing system, wherein the data processing system includes two or more processors and an original limit is placed on the number of processors that are available for use, the method comprising:

providing an authorization key to the data processing system, wherein the authorization key specifies a new limit on the number of processors that are available for use; and

increasing the performance level of the data processing system by activating one or more of the processors that were previously unavailable for use before a next time the data processing system is powered up.

As noted above, claim 24 recites the step of increasing the performance level of the data processing system by activating one or more of the processors that were previously unavailable for use before a next time the data processing system is powered up. The present specification teaches that the performance level of the customer's data processing system can be changed while operating at full production capacity, and that such a system may help provide uninterrupted growth capacity, temporary increased processing capacity to support high demand peak periods, and disaster recovery support should a primary data processing system become destroyed.

In contrast with the foregoing, Campbell et al. states:

Upon reference to the foregoing those skilled in the art will appreciate that by providing selected "personality" data within nonvolatile storage devices within a data processing system and accessing that data each time power is applied to the data processing system, the content of a personality register may be controlled which may then be utilized to enable and/or disable various functional characteristics of the data processing system, permitting the manufacturer to provide a variety of functional characteristic capabilities within a single data processing system, without physical or mechanical manipulation.

(Emphasis Added)(Campbell et al., column 9, lines 54-65). As can be seen, Campbell et al. appear to teach that the "functional characteristics" of the data processing system are only changed upon power up of the data processing system. Such a system, however, may not provide many of the advantages of the present invention - such as providing uninterrupted growth capacity, temporary increased processing capacity to support high demand peak periods, and disaster recovery support should a primary data processing system become destroyed - at least not to the same degree as the present invention. In addition, Campbell et al. do not appear to disclose or suggest any way to increase the performance level of the data processing system by activating one or more of the processors that were previously unavailable for use before a next time the data processing system is powered up.

Fenstemaker does not appear to add anything in this regard. As noted above,

Fenstemaker et al. relates to changing the <u>functionality</u> of an ultrasound imaging system,
and not the <u>performance level</u> of a data processing system as recited in claim 24. As noted
above, there is a big difference between enabling the use of a particular transducer or an
additional software routine in an ultrasound machine versus increasing the <u>performance</u>

<u>level</u> of a data processing system - such as by increasing the operating speed and/or number
of processors that are available to the operating system. Fenstemaker et al. do not appear to
teach or suggest anything that relates to increasing the <u>performance level</u> of a data
processing system.

Moreover, there does not appear to be any motivation to make the combination of Campbell et al. and Fenstemaker et al., as the Examiner suggests. The Examiner states that it would have been obvious to one of ordinary skill in the art to increase the number of

processors in Fenstemaker's system to allow for further profitability and control. However, there does not appear to be any need to increase the number of processors in Fenstemaker et al. As noted above, Fenstemaker et al. relates to changing the <u>functionality</u> of an ultrasound imaging system, and not the <u>performance level</u> of a data processing system as recited in claim 24. Fenstemaker et al. does not appear to need any additional processing power. As such, it is unclear how increasing the number of processors in Fenstemaker's system allows for further "profitability and control", as the Examiner suggests. In view of the foregoing, claim 24 is believed to be clearly patentable over Campbell et al. in view of Fenstemaker et al. For similar reasons, as well as other reasons, dependent claims 25-26 and 33-40 are also believed to be clearly patentable over Campbell et al. in view of Fenstemaker et al.

Specifically with respect to claim 33, and as detailed above, Campbell et al. do not disclose or suggest performing the providing and increasing steps while the data processing system is in use. As noted above, Campbell et al. appear to teach changing the "functional characteristics" of the data processing system upon power up of the data processing system, which by definition, cannot be "while the data processing system is in use". Fenstemaker et al. does not appear to add anything in this regard.

Specifically with respect to claim 35, Campbell et al. do not appear to teach or suggest using the <u>operating system of the data processing system</u> to activate one or more processors. Again, Fenstemaker et al. does not appear to add anything in this regard.

With respect to claim 36, Campbell et al. do not appear to teach or suggest a data processing system wherein the operating system maintains a table that includes entries that identify the processors in the data processing system, and further identify which processors

are available for use. Likewise, and with respect to claim 37, Campbell et al. do not appear to teach or suggest changing selected entries in the table to indicate that one or more of the processors that were previously unavailable for use are now available for use. With respect to claim 38, Campbell et al. do not appear to teach or suggest an operating system that detects the changes to the table, and ups the processors that are indicated as available for use that were previously unavailable for use. Further, and with respect to claim 39, Campbell et al. do not appear to teach or suggest changing selected entries in a table so that selected processors that are available for use are de-activated and become unavailable for use to return to the original limit of the number of processors that are available for use. Finally, with respect to claim 40, Campbell et al. do not appear to teach or suggest an operating system that detects the changes to the table, and downs the processors that are indicated as unavailable for use.

In all of these cases, Fenstemaker et al. do not appear to teach or suggest what Campbell et al. lacks. Thus, for these additional reasons, as well as other reasons, claims 33-40 are all believed to be clearly patentable over Campbell et al. in view of Fenstemaker et al. If the Examiner elects to maintain these rejections, Applicants respectfully request that the Examiner specifically point out where in Campbell et al. or Fenstemaker et al. each and every one of these elements is shown.

In paragraph 19 of the Office Action, the Examiner rejected claims 27-32 under 35 U.S.C. § 103(a) as being unpatentable over Fenstemaker in view of Campbell. For similar reasons to those given above, as well as other reasons, claims 27-32 are believed to be clearly patentable over Fenstemaker in view of Campbell.

In view of the foregoing, Applicants believe that all pending claims 1-40 are in

condition for allowance. Reexamination and reconsideration are respectfully requested. If the Examiner believes it would be beneficial to discuss the application or its examination in any way, please call the Applicant at (612) 359-9348.

Respectfully submitted,

Lee B. Hansen et al.

By their attora

Dated: <u>KU134+ 13</u>

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